A Structured Parallel Periodic Arnoldi Shooting Algorithm for RF-PSS Analysis based on GPU Platforms

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ABSTRACT

The recent multi/many-core CPUs or GPUs have provided an ideal parallel computing platform to accelerate the time-consuming analysis of radio-frequency/millimeter-wave (RF/MM) integrated circuit (IC). This paper develops a structured shooting algorithm that can fully take advantage of parallelism in periodic steady state (PSS) analysis. Utilizing periodic structure of the state matrix of RF/MM-IC simulation, a cyclic-block-structured shooting-Newton method has been parallelized and mapped onto recent GPU platforms. We first present the formulation of the parallel cyclic-block-structured shooting-Newton algorithm, called periodic Arnoldi shooting method. Then we will present its parallel implementation details on GPU. Results from several industrial examples show that the structured parallel shooting-Newton method on Tesla’s GPU can lead to speedups of more than 20× compared to the state-of-the-art implicit GMRES methods under the same accuracy on the CPU.

1. INTRODUCTION

The recent advance in radio-frequency (RF) and millimeter-wave (MM) integrated circuits (ICs) operating at 60 GHz [1–4] can provide much higher data-rate than today’s mobile devices for future smart-mobile applications. However, the design for RF/MM-IC front-end circuits at this scale is very challenging [5–11]. At the scale of 60 GHz, all active and passive devices are closely coupled and the resulting postlayout circuit model has a drastically increased complexity. Moreover, in order to follow the high-frequency carrier, traditional transient analysis requires using small time-steps and hence results in a long simulation time.

The analysis of RF/MM-IC systems is notoriously difficult for speedup as accuracy can not be compromised for a precision design. Worse, its design complexity increased drastically since all active and passive devices are no longer separated but coupled. There are three approaches to numerically find the PSS solution [5–10]: finite difference method (FDM), harmonic balance (HB) method, and shooting-Newton method. Compared to the first two approaches, shooting method has a better convergence for strongly nonlinear circuits because the underlying transient analysis has an adaptive time-step control. However, the resulting Jacobian (sensitivity matrix) during the shooting-Newton method is usually a large-scale dense matrix. The iterative GMRES with the use of a standard Krylov-subspace [12, 13] and an implicit matrix formulation [8] partially alleviate the analysis cost of shooting-Newton method.

As such, the performance and capacity required to successfully verify a full-chip design strains the limit of the existing single-core based computing approach. Fortunately, for the sake of harvesting high-performance scalability from parallelism, modern computer architecture has shifted towards designs that employ multiple processor cores on a chip, so called multi-core processors or chip-multiprocessors (CMP) [14, 15]. For example, the graphic processing unit (GPU) is one of the most powerful many-core computing systems in use. The general purpose computing has been around for decades and we have many well-developed tools to improve the performance scalability of developing new scientific computing for a CPU. Only recently though, has GPU computing become a viable platform for solving large-scale computationally intensive scientific computing problems. In addition to being less of a mature platform, current GPUs have some architectural constraints that make it a little more difficult to exploit their full potential. Fortunately, tools for general-purpose GPU computing such as CUDA [16], Stream SDK [17] and OpenCL [18] allow much easier access to the computing capabilities of the modern graphics processor. A fast RF/MM-IC analysis based on the massive parallelism using GPUs is thereby urgently needed to handle the design complexity and reduce the design cycle.

To harness the power of the underlying hardware parallelism of the recent multi-core and GPU processors, an algorithmic innovation is required to develop an efficient parallel analysis. A simple parallel implementation of the matrix-vector multiplication can be definitely beneficial when it is unclear how to map the matrix-vector multiplication in shooting-Newton to the GPU hardware. In this paper, we further point out that studying problem structure is one effective way to explore parallelism. Since most RF/MM ICs are with periodic inputs and can be characterized as a periodic steady state (PSS) problem, the state matrix generally shows a periodic-block-matrix structure, or periodic structure, to be considered when developing algorithms for RF simulation. Without exploration of the structure of state space, the remaining part of the traditional PSS problem can not be fully parallelized. We employ the time-domain shooting-Newton method as an example to demonstrate how to improve the efficiency by identifying parallelism from the RF problem structure, i.e., periodic structure, which brings inherent parallelism from the RF problem structure but was ignored by the existing non-structured algorithms.

In this paper, we formulate the structured shooting-Newton method with the use of one periodic Arnoldi shooting (PAS) algorithm, and further map the related computational operations of PAS on the latest NVIDIA Tesla GPU platform. The new PAS algorithm can identify the periodic structured Krylov-subspace and hence can lead to highly parallel periodic structured operations. The existing study in [19, 20]
has an initial exploration of the structured Krylov-subspace and its parallization. However, [19] has not exposed how to identify the periodic structured Krylov space during the shooting-Newton, and [20] has not discussed the improved iteration convergence and the structured parallization. The primary contribution of this paper is the development of the PAS-based shooting-Newton method on the state-of-art GPU of Tesla-C1060 card with CUDA. Experiment results show superior performance improvement when compared to the sequential non-structured shooting-Newton method such as up to 27× runtime speedup.

2. TRADITIONAL SHOOTING NEWTON

In this paper, we review the shooting-Newton-based periodic steady state (PSS) analysis, which is to be parallelized.

A nonlinear RF/MM-IC circuit can be described in general by a differential-algebra-equation (DAE) shown below,

\begin{equation}
    f(x(t), t) = \frac{d}{dt}q(x(t)) + j(x(t)) + u(t) = 0.
\end{equation}

where \( x \in \mathbb{R}^N \) is the state variable vector including nodal voltages and branch currents, \( j(x(t)) \) is for nonlinear node currents, \( q(x(t)) \) is for nonlinear node charges or fluxes, and \( u(t) \) is for external periodic sources.

A state-transition function \( \phi_T(x_0, t_0) \) is the solution of (1) at \( t_0 + T \), starting from a guessed initial state \( x(t_0) \) at \( t_0 \), or

\begin{equation}
    x(t_0 + T) = \phi_T(x(t_0), t_0).
\end{equation}

Then two-point boundary constraint for PSS problem is

\begin{equation}
    \phi_T(x(0), 0) = x(0)
\end{equation}

for one period with \( t_0 = 0 \). A shooting-sensitivity, or called shooting-Jacobian, can be further defined by

\begin{equation}
    J_{\phi_T}(x(0), 0) = \frac{dx(t)}{dx(0)} = \frac{d\phi_T(x(0), 0)}{dx(0)}.
\end{equation}

Since (3) is in the nonlinear algebraic form, Newton iteration is deployed to solve for \( x(0) \) or denoted as \( x_0 \) (and \( x(t) \) as \( x_T \)). Such a method is the so-called shooting-Newton algorithm [5, 7, 8, 10] with the following Newton iteration

\begin{equation}
    x_{k+1} = x_k - J_{\phi_T}^{-1}(x_k - x_{k-1}) \left[ \phi_T(x_k - x_{k-1}) \right]^{-1} - J_{\phi_T}(x_k, 0)
\end{equation}

where \( I \) is the identity matrix and \( k \) is the iteration number.

Specifically, \( J_{\phi_T} \) is obtained by the following manner. First one integrates the DAE (1) in one period, for example, by Backward-Euler method

\begin{equation}
    \frac{1}{h_j} \left[ q(x_j) - q(x_{j-1}) \right] + j(x_j) + u_j = 0,
\end{equation}

where \( h_j \) is the \( j \)th time-step: \( h_j = t_j - t_{j-1} \), with \( t_0 = 0 \) and \( t_T = T \) for total \( p \) time-steps. As such, the linearized (6) at \( t^0 \) transient-Newton iteration becomes

\begin{equation}
    G(x_j) - J_{\phi_T}(x_j, 0) = \frac{C(x_j)}{h_j}
\end{equation}

where \( G(x_j) = \frac{d q(x_j)}{dx} \) and \( C(x_j) = \frac{d u}{dx} \) with \( h_j = \frac{1}{h_j} \left( q(x_j - x_{j-1}) - j(x_j - x_{j-1}) \right) - u_j \) (7)

with \( G(x_j) = \frac{d q(x_j)}{dx} \) and \( C(x_j) = \frac{d u}{dx} \). Moreover, when differentiating (6) with respect to \( x_0 \), the following relation can be obtained

\begin{equation}
    G(x_j) + \frac{C(x_j)}{h_j} \frac{dx_j}{dx_0} = \frac{C(x_{j-1})}{h_j} \frac{dx_{j-1}}{dx_0}.
\end{equation}

When this is applied recursively following the chain-rule for all time steps in one period, one can eventually obtain the shooting-Jacobian by

\begin{equation}
    J_{\phi_T} = \prod_{j=1}^{p} \left[ G(x_j) + \frac{C(x_j)}{h_j} \right]^{-1} \frac{C(x_{j-1})}{h_j}.
\end{equation}

Note that as the transient analysis is first applied, the matrices \( G(x_j) + \frac{C(x_j)}{h_j} \), \( j = 1, \ldots, p \), are already available and are stored as LU-factored sparse matrices. Therefore the computational cost is mainly from the \( n^2 \) backward and forward evaluations of forming the dense \( J_{\phi_T} \), and the \( n^3 \) flops of the direct LU-factorization of (5).

3. GMRES WITH NON-STRUCTURED KRYLOV SUBSPACE

3.1 Conventional Arnoldi Method in GMRES

To reduce the computation cost of solving the shooting-Newton update equation (5), the traditional non-structured Krylov-subspace based GMRES method can be applied. The GMRES method is an iterative method for solving systems of large scale linear equations \( Ax = b \) where \( A \) is dense.

Algorithm 1 shows one standard Krylov-subspace based GMRES [12, 13]. The \( m \)th order orthonormal vector basis \( V_m = [v_1, v_2, \ldots, v_m] \) is needed to span the \( m \)th order Krylov-subspace, and hence, to solve the linear equation is equivalent to find the optimal coefficient \( y \) to calculate approximate solution by linear combination \( x_m = V_m^T y \), such that the norm \( ||b - A x_m||_2 \) attains its minimum. As such, the approximate solution \( x_m \) is contained in

\begin{equation}
    K_m = \text{span}\{b, Ab, A^2b, \ldots, A^{m-1}b\},
\end{equation}

whose orthonormal basis is \( V_m \).

Algorithm 1 Non-Structured GMRES with conventional Arnoldi method

**Input:** \( A \) and \( b \)

1: Initialize: \( r_0^0 = b - Ax^0 \) and \( h^0 = ||r^0||_2, v^1 = r^0/h^0 \)

2: while \( h^{i+1} > tol \) and \( i < \text{maxIter} \) do

3: Set \( h^i = V_i^T A v^i \)

4: Set \( w = A v^i - V_i^T h^i \)

5: Set \( g^i = ||w||_2, H^i = [H^i_{j=1} h^i, 0, 0] \)

6: Update: \( v^i = w/g^i, V^i = [V_i^{i-1}, v^i] \)

7: Minimize: \( ||h^i - A V^i y||_2 \) to find \( y \)

8: \( x^i = x^i + V^i y \)

9: end while

**Output:** \( x = x^i \) \((x \in \mathbb{R}^N)\) such that \( Ax \approx b \)

Moreover, to generate the Krylov-subspace in GMRES, the Arnoldi algorithm [12, 13] can be used to form the orthonormal basis \( V_m \) of the Krylov-subspace. Each Arnoldi iteration generates a new vector and is then appended to the previous Krylov-subspace basis \( V_{m-1} \) to form a new orthonormal basis \( V_m \). The Arnoldi algorithm also creates an upper Hessenberg matrix \( H_m \) of size \((m+1)\times m\), which is used to check the solution at the current iteration. However, as discussed later in this paper, this standard GMRES procedure ignores the periodic structure of matrix for the PSS RF problem, and hence might result in more iterations and low parallel scalability.

3.2 Matrix-free in GMRES


For the PSS shooting-Newton method, the Krylov-subspace based GMRES can be employed to calculate the state variable $x$, when the state matrix $A$ and input $b$ are given by $A = I - J_{\phi_T}$ and $b = \phi_T(x_{n-1}) - x_n$. GMRES can be embedded into the shooting-Newton algorithm. However, explicitly forming the matrix $A$ involves $n^2$ evaluations and $n^2$ multiplications for $Av_k$ for each GMRES iteration. To further reduce the computational cost, one needs to avoid the explicit formulation of matrix $A$.

The work in [8,10] introduced a matrix-free approach to directly calculating the matrix-vector-multiplication (MVP) $Av_k$ without forming $A$.

This is realized in Algorithm 2. The matrix-free method calculates the product of $J_{\phi_T}v^k$ instead of $Av^k$ by iteratively reusing the pre-factored chain-rule values of each

$$G(x_j) + \frac{C(x_j)}{h_j} - \frac{1}{h_j} C(x_j) w$$

Here, the shift of $I$ to form $A$ from $J_{\phi_T}$ can be easily corrected when updating $v^k$.

**Algorithm 2** Matrix-free to replace Line 4 in Algorithm 1

**Input:** Previous basis $v^k$ and prefactorized matrices (10)

1. $u = v^k$
2. for $j = 1 : p$
3. $u = \left(G(x_j) + \frac{1}{h_j} C(x_j)\right)^{-1} \frac{1}{h_j} C(x_j) w$
4. end for
5. $v = v^k - u$

**Output:** $u = Av^k = (I - J_{\phi_T})v^k$

### 4. GMRES WITH PERIODIC STRUCTURED KRYLOV SUBSPACE

We observe that the Krylov-subspace of a periodic system usually contains a periodic-block-matrix structure (or periodic structure). We show that if one can identify the periodic structure of the Krylov-Subspace, the convergence and efficiency of the GMRES can be improved. As discussed later, the proposed periodic Arnoldi shooting method, PAS, can further maximize the parallelism to support the shooting-Newton method on GPUs.

#### 4.1 Periodic Structured Krylov Subspace

Note that the RF/MMIC system with periodic inputs is in fact a periodic system. Hence the associated Krylov-subspace would also exhibit a periodic structure. As such, it inspires us to exploit the structure-preserved Krylov-subspace method during the GMRES iteration.

Let's first identify the structure of the underlying Krylov-subspace. For $j = 1, \ldots, p$ time-step, defining

$$G_j = G(x_j), \ C_j = C(x_j), \ A_j = \left[G_j + \frac{C_j}{h_j}\right]^{-1} \frac{1}{h_j} C(x_j) w$$

the shooting-Jacobian $J_{\phi_T}$ in (9) becomes

$$J_{\phi_T} = A_p \times A_{p-1} \times \cdots \times A_1.$$  \hspace{1cm} (12)

As shown in [20], the multiplied product $J_{\phi_T}$ has an invariant subspace identical to the periodic block-structured matrix in 13, which has a periodic structured Krylov-subspace.

$$J = \begin{bmatrix} 0 & A_p \\ A_1 & \ddots \\ & \ddots & \ddots \\ & & A_{p-1} & 0 \end{bmatrix}$$  \hspace{1cm} (13)

Figure 1: The comparison of standard Krylov-subspace and $p$-periodic-block-structured Krylov-subspace. Order $m = 4$, and $p = 3$ time-steps.

Accordingly, as shown in Algorithm 3, one can develop a periodic Arnoldi method [20] based on the periodic Krylov-subspace, i.e., the block matrices $V_i^m$ and $H_i^m$, $j = 1, \ldots, p$. Here, the subscript $j$ denotes the index of the periodic blocks ($j = 1, \ldots, p$), and the superscript $i$ denotes the index of the order of the Krylov-subspace ($i = 1, \ldots, m$). Different from Algorithm 1, the periodic structure of the generated Krylov-subspace is preserved in Algorithm 3 because each orthonormalized base $V_i^j$ is constructed separately for each $A_i$. In contrast, the base of the Krylov-subspace in Algorithm 1 is generated for the overall $J_{\phi_T} = A_1 \times \cdots \times A_p$ in Algorithm 1. As a result, the periodic structure of the underlying Krylov-subspace is destroyed in Algorithm 1.

#### 4.2 Convergence Improvement

Since the periodic Arnoldi method can identify the Krylov-subspace with the consideration of the periodic structure, in the following, we further explain that a better convergence can be observed in comparison with the non-structured Krylov-subspace based method. The primary observation in Section 4.1 is to illustrate that the underlying Krylov-subspace of shooting-Newton implicitly has the periodic structure as shown in (13). The subspace at each time-step are calculated from each those $A_i$ and right-hand-side $b_i$ vectors ($i = 1, \ldots, p$), which preserves the periodic structure in (13).

Take $m = 4$ and $p = 3$ for example, in the traditional
Algorithm 3 A matrix-free periodic Arnoldi method

1: Inputs: $A_j$ by pre-factorized matrices ($10$ ($j = 1, \ldots, p$)
2: Initialize $V_j^p$ by $v_0$ and $V_j^0$ by 0
3: while $h^{i+1} > tol \& \& i < maxIter$ do
4:     for $j = 1 : p$ do
5:         Set $h_j^i = V_{j+1}^i A_j v_j^i$
6:         Set $w = A_j v_j^i - V_{j+1}^i h_j^i$
7:         Set $g_j^i = \|w\|_2$, $H_j^i = \begin{bmatrix} H_j^{i-1} & * \\ 0 & \end{bmatrix}$
8:         $v_{j+1}^i = w / g_j^i$, $V_{j+1}^i = [V_{j+1}^{i-1}, v_{j+1}^i]$
9:     end for
10: end while
11: Outputs: Block matrices $V_j^m$ and $H_j^n$ ($j = 1, \ldots, p$)

GMRES, the solution is

$$x^4 \in \text{span}(\begin{bmatrix} b_1 \\ A_1 b_2 \\ A_2 b_3 \\ A_3 b_4 \end{bmatrix}, \begin{bmatrix} b_1 \\ A_1 b_2 \\ A_2 b_3 \\ A_3 b_4 \end{bmatrix}, \begin{bmatrix} b_1 \\ A_1 b_2 \\ A_2 b_3 \\ A_3 b_4 \end{bmatrix})$$

while the $p$-periodic-block-structured method has the solution

$$x^4 = \begin{bmatrix} x_1^4 \\ x_2^4 \end{bmatrix} \in \mathcal{K}_m = \text{span}(b_1, A_1 b_2, A_2 b_3, A_3 b_4)$$

Their differences can be clearly illustrated by Figure 1. Due to the structure-preservation, the periodic Arnoldi shooting (PAS) method with ($p$-periodic-block-matrix structure) shows higher freedom to search the approximate solution from a $p$ times larger dimension of the subspace than the traditional one. Therefore, PAS can significantly reduce the iteration number under the same accuracy, which in turn has a reduced runtime as observed by the experiments. In the following part, we further show that such a structured subspace can also lead to a number of structured operations, which can easily mapped on the parallel GPU platform.

4.3 PAS Parallelization on GPU

In this part, we further discuss how to have a highly parallel implementation of PAS-based shooting-Newton method on GPU.

We first describe the processor and memory architectures of recent Nvidia Tesla GPU (T10 GPU). One T10 is a streaming multiprocessor (SM) and has 8 streaming processors (SPs or cores) and 2 special function units (SFUs). It has 16 KB R/W shared memories. It can have 1024 threads (128 threads/SP) and 16K registers. Threads are grouped by thread block and up to 512 threads are allowed per thread block. A SM can hold up to 8 thread blocks. A GPU grid (one T10) consists of many blocks with different kinds of memories such as local memory (local to each thread), shared memory (shared by threads in a block), global memory, constant memory and texture memory shared by threads by all the threads in a GPU grid. Those memories have different latencies. In T10 GPU, threads within a thread block are grouped into warps of 32 parallel threads, which is the scheduling unit of coalescing memory access.

Note that tremendous amount of matrix vector multiplications (MVMs) are employed in GMRES. Given sufficient memory, it allows us to keep our values of matrix $A$ as well as residual vector $v^i$ and in GPU memory without constantly swapping them in and out of memory for each iteration. To implement such a common MVM operation on GPU, we notice that memory bandwidth is a critical part of performance bottleneck and must be considered when programming applications for GPU computing. One of the key aspects to making the MVM in GMRES performed well is to limit the number of memory transactions, which are required without compromising the amount of work to be offloaded to the GPU. Improving the efficiency of memory transactions within one GPU, the computing application could observe the performance difference between 10% and 50% of the theoretical computing limit of one GPU.

A diagram on how memory transfers are used efficiently for our GPU-based GMRES implementation is shown in Fig. 2. Once the initial transfer of the matrix $A$ and vectors $x$ and $b$, the only time a memory transfer is made during the main loop of the GMRES implementation is when building the Hessenberg matrix during the orthonormalization process. During this step, the $k + 1$ column of $H$ is transferred to the CPU from which a Least-Square minimization is performed to see if the desired tolerance of our residual has been met. As can be seen in Fig. 2, all matrix-vector, vector-vector, and scalar-vector operations are done without any unnecessary memory transfers. Other than the MVM operation, all of these operations can be done with the well optimized CUDA implementation of Basic Linear Algebra Subprograms (CUBLAS).

More importantly, because of the independent calculation of the new basis vector inside each subspace for different time-step $j$ ($j = 1, \ldots, p$) in Algorithm 3, the structure-preserved Arnoldi iteration can be highly implemented on GPU in parallel. For the initialization part, the memory for basis vectors and Hessenberg matrices are allocated. While it is a wise strategy to keep all basis vectors $V_j^{m+1}$ in GPU memory, since Hessenberg matrices are needed in solving the least squares problem. In order to calculate the final solution, because of the serial nature of the least squares problem, $H_j^n$’s are saved in host memory instead of GPU memory. There is another noteworthy detail about the application of MVM to the calculation of new basis vector. Same as the matrix-free GMRES method, we also use the pre-factorized LU matrices from the linearization process of device models, since these matrices are required by SPICE on each time-step. So once they are available, we save them into compressed row major form (CSR) sparse matrix. Hence, it accelerates periodic structured GMRES when using GPU-MVM to generate the new vector.

![Diagram showing memory transfers and CUDA usage in GMRES implementation.](image-url)
Table 1: Initial GMRES GPU vs. CPU test

<table>
<thead>
<tr>
<th>Name</th>
<th>N(dim)</th>
<th>density</th>
<th>Time (ms) CPU</th>
<th>Time (ms) GPU</th>
<th>Speedup</th>
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<tbody>
<tr>
<td>MNA</td>
<td>10,000</td>
<td>4.88%</td>
<td>436.8</td>
<td>140</td>
<td>23.8</td>
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<tr>
<td>Cage11</td>
<td>39,082</td>
<td>0.04%</td>
<td>216.3</td>
<td>56</td>
<td>3.8</td>
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<tr>
<td>laminar</td>
<td>67,173</td>
<td>0.09%</td>
<td>15921</td>
<td>2030</td>
<td>7.84</td>
</tr>
<tr>
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<td>115,967</td>
<td>0.007%</td>
<td>700.8</td>
<td>123</td>
<td>5.66</td>
</tr>
</tbody>
</table>

5. NUMERICAL EXPERIMENTS

The proposed periodic Arnoldi shooting is implemented in C++ within SPICE on GPU and is called GPU-PAS-GMRES. The GPU cards we use for all the experiments here is NVIDIA’s Tesla C1060, which contains 30 multiprocessors (totally 240 cores) and works in a 1.30 GHz clock rate with 4GB on-chip memory. For the CPU-server, an Intel core-duo server with 2.4GHZ CPU and 4GB memory is used.

The matrix-free GMRES with the non-structured Krylov-subspace, are implemented for the comparison. The non-structured Krylov-subspace is identified through the least-minimum-square constraint using a template from [21]. The matrix-free GMRES is implemented exactly following the procedure described in [8, 10], and is called CPU-GMRES and GPU-GMRES in the following. The GMRES iteration tolerance is set as $10^{-6}$ for voltage nodes. We compare the accuracy and runtime with a scalability study using 5 industrial analog/RF examples, including a CMOS-dc-converter, a CMOS-frequency-multiplier, a BJT-mixer, a CMOS-low-noise-amplifier (LNA), and a CMOS-switch-cap. We increase the complexity by adding extracted parasitics.

5.1 GPU-based Matrix-Vector-Multiply

Since the traditional GMRES spends really a long time to generate the sensitivity matrix, and use that dense matrix to calculate new basis vector each time in the Arnoldi iteration, this method is not suitable for large scale RF circuit simulation. On the contrary, the most up-to-date matrix-free method utilizes the prefactorized sparse LU matrices inside the Arnoldi iteration for matrix vector multiplication. Thus there is a evident speedup compared to explicit GMRES.

When the GPU parallelization is considered, we first show the results of the GPU-based GMRES solver vs. the CPU-based GMRES solver, mainly to demonstrate the performance matrix-vector-multiply (MVM). The performance if our current implementation on the GPU achieves the convergence in a similar number of iterations to that of a pure CPU implementation. The benchmark matrices used in the experiment here come from the University of Florida Sparse Matrix Collection [22]. As shown in Table 1, using a non-optimized version of the MVM for the GPU, we obtained 4× speedup on average over the CPU-based GMRES. With further optimization of GPU and CPU communication discussed in Section 4.3, the GPU-based GMRES is capable of 15× speedup on average over the GPU-based GMRES on the same matrices.

5.2 GPU-based PAS-GMRES on GPU

We first show details of one examples with waveform accuracy comparison. The example is a CMOS switch-cap with 654 states and a carrier input frequency of 1GHz. Fig. 3 shows two agreed periodic-steady-states at two nodes. For this example, the GPU-PAS-GMRES method converges in 3 iterations using 0.04s and the CPU-GMRES method converges in 11 iterations using 52.5s.

In the following, we further demonstrate the speedup of the GPU-parallization of the PAS-GMRES. When the GPU parallelization is further applied to the PAS-GMRES solver, we can save the computation time further, and the speedup of parallel GPU-PAS-GMRES over the GPU-GMRES (matrix-free) is up to 27× for these examples. As such, the new parallel GPU-PAS-GMRES solver is more scalable and will yield more speedups when the larger post-layout RF circuits are available.

Table 2: Run time comparison of different GMRES method

<table>
<thead>
<tr>
<th>Ckt</th>
<th>#Eq</th>
<th>Time (s) CPU</th>
<th>Time (s) GPU</th>
<th>Time (s) GPU-PAS</th>
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</thead>
<tbody>
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<td>dc-converter</td>
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<td>30.8</td>
<td>1.13</td>
<td>0.10</td>
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<td>BJT-mixer</td>
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<td>52.5</td>
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<tr>
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<td>139</td>
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<tr>
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<td>1055</td>
<td>1238</td>
<td>27.2</td>
<td>1.04</td>
</tr>
</tbody>
</table>

6. CONCLUSION

In this paper, we have developed a structured shooting algorithm that can fully exploit parallelism in periodic steady state (PSS) analysis. The proposed method utilizes the periodic structure of the state matrix of RF/MM-IC, and the periodic Arnoldi based shooting-Newton method, called PAS. PAS leads to the robust parallel shooting-Newton algorithm with improved convergence and parallelism. Due to the structure-preservation, PAS is further mapped on a GPU platform with additional speedup observed. We have showed the implementation detail such as the optimization of memory allocation between CPU and GPU for this purpose. Experimental results from several industrial examples show that when compared to the state-of-the-art implicit GMRES methods under the same accuracy, the GPU-PAS based shooting-Newton method can result in up to 27× speedup.

7. REFERENCES


