

# International Workshop on Emerging Circuits and Systems (IWECS'09)

Sponsored by  
ASIC & System State-key Lab of Fudan University, China  
National Natural Science Foundation of China (NSFC)  
National Science Foundation of USA (NSF)

## IWECS'09 Program

[Fu-Xuan Hotel, 400 Guo-Ding Rd, Shanghai, China](#)

(复宣酒店, 国定路 400 号, 上海)

### July 5, Sunday 2009

Hotel Check-in and Registration

### July 6, Monday

Time	Content	Speakers	Remark
8:00am-9:00am	Breakfast		In the hotel
<b>Session 1</b>	<b>Chair: Sheldon Tan</b>		
9:00am-9:15am	Welcome speech Microelectronic Research and Education In Fudan University	Xuan Zeng Fudan Univ., China	
9:15am-9:30am	Opening Speech Welcome and Introduction to the IWECS'09	Sheldon Tan, UC Riverside, USA State-key Lab of ASIC & System, Fudan University	
9:30am-10:00am	<a href="#">Dependable VLSI Platform Using Robust Fabrics</a>	Hidetoshi Onodera, Kyoto University, Kyoto, Japan	
10:00am-10:30am	<a href="#">DFM Research Activities in Fudan</a>	Hengliang Zhu	

		Fudan Univ., China	
<b>10:30am-11:00am</b>	<b>Coffee break</b>		
11:00am-11:30am	<a href="#">Some design considerations for ultra-high-frequency VLSI modeling and simulation</a>	Ngai Wong, Hong Kong University, Hong Kong	
11:30am-12:00am	<a href="#">Process Variation Robust Digital Circuit Design Techniques</a>	Charlie Chen, National Taiwan University, Taiwan	
<b>12:00pm-1:00pm</b>	<b>Lunch</b>		<b>In the hotel</b>
<b>Session 2</b>	<b>Chair: Lei He</b>		
1:30pm-2:00pm	<a href="#">Chip-Level parameterized thermal modeling for multi-core microprocessor design</a>	Sheldon Tan, UC Riverside, USA State-key Lab of ASIC & System, Fudan University	
2:00pm-2:30pm	<a href="#">Experimental Signal Integrity Verification of High-Speed Interconnect Lines</a>	Yungseon Eo, Hanyang University, Ansan, Korean	
2:30pm-3:00pm	<a href="#">GPU-friendly fast Poisson solve for regular power grid network analysis</a>	Jin Shi, Tsinghua University, China	
<b>3:00pm-3:30pm</b>	<b>Coffee break</b>		
3:30pm-4:00pm	<a href="#">Bridging the gap between laboratory measurement and simulation model</a>	Takashi Sato, Kyoto University, Kyoto, Japan	
4:00pm-4:30pm	<a href="#">Statistical Modeling and Analysis Techniques for Nano-Scale Circuits</a>	Guoyong Shi, Shanghai Jiaotong University, China	

	<a href="#">with Significant Variations</a>		
<b>4:30pm-5:30pm</b>	<b>Break</b>		
<b>5:30pm-7:30pm</b>	<b>Dinner</b>		<b>Lang Yi Fang, Zhengda Plaza</b>
<b>8:00pm-9:00pm</b>	<b>Pujiang River Cruise</b>		

### July 7, Tuesday

<b>Time</b>	<b>Content</b>	<b>Speakers</b>	<b>Remark</b>
<b>8:00am-9:00am</b>	<b>Breakfast</b>		<b>In the hotel</b>
<b>Session 3</b>	<b>Chair: Gengsheng Chen</b>		
9:00am-9:30am	Integration of MEMS components on CMOS-grade wafers for microelectronic industry	Xinxin Li, Shanghai Institute of Microsystem and Information Technology, China	
9:30am-10:15am	Spotlight on the China's IC Industry	William Lee SMIC	
10:15am-10:45am	<a href="#">Modeling of Nano-scaled Analog Device</a>	Junning Chen Anhui University, China	
<b>10:45am-11:15am</b>	<b>Coffee Break</b>		
11:15am-11:45am	<a href="#">Leakage power and NBTI-induced Aging Co-optimization for Digital ICs</a>	Yu Wang, Tsinghua University, China	
11:45am-12:15pm	<a href="#">Fast localized statistical 3D thermal analysis for SoC Designs</a>	Zuying Luo, Beijing Normal University, China	
<b>12:15pm-1:15pm</b>	<b>Lunch</b>		<b>In the hotel</b>

<b>Session 4</b>	<b>Chair: Sheldon Tan</b>		
1:30pm-2:00pm	<a href="#"><u>Robust (FPGA) Circuits and Systems</u></a>	Yu Hu, UCLA & University of Alberta, USA/Canada	
2:00pm-2:30pm	<a href="#"><u>Multicore parallel algorithms for CAD applications</u></a>	Hai Zhou, Northwest University, USA	
2:30pm-3:00pm	<a href="#"><u>Fast FPGA Placement Based on Quantum Genetic Algorithm</u></a>	Lingli Wang, Fudan University, China	
3:00pm-3:30pm	Closing speech EDA industry in China	Weiping Liu CEC Huada Electronic Design Cor. Ltd.	
<b>3:15pm-6:30pm</b>	<a href="#"><u>Skywalk, Shanghai World Financial Center</u></a>		
<b>6:30pm-8:00pm</b>	<b>Buffet</b>		<b>Holiday Inn Crown Plaza</b>

Contact workshop secretary Ms. Wei Xu ([wei\\_xu@fudan.edu.cn](mailto:wei_xu@fudan.edu.cn)) for more information (hotel reservation and travel issues).

**The program will subject to further change, check the latest Information at**

[http://www.ee.ucr.edu/~stan/iwecs\\_workshop/2009/iwecs09\\_programme.pdf](http://www.ee.ucr.edu/~stan/iwecs_workshop/2009/iwecs09_programme.pdf)

**Organizers:**

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